

	Application No.	Applicant(s)
	10/710,475	YEN, YAO TUNG
Notice of Allowability	Examiner	Art Unit
	Daniel D. Chang	2819
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	(OR REMAINS) CLOSED in this a or other appropriate communication GHTS. This application is subject	pplication. If not included on will be mailed in due course. THIS
1. Application is responsive to Application filed 7/14/	<u>′04</u> .	
2. 🛮 The allowed claim(s) is/are <u>1-25</u> .		
 Acknowledgment is made of a claim for foreign priority ur a) ☐ All b) ☐ Some* c) ☐ None of the: 	nder 35 U.S.C. § 119(a)-(d) or (f).	
 Certified copies of the priority documents have been received. 		
2. Certified copies of the priority documents have been received in Application No		
Copies of the certified copies of the priority do	cuments have been received in thi	s national stage application from the
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		y complying with the requirements
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give		
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.	
(a) I including changes required by the Notice of Draftspers	on's Patent Drawing Review (PTG	O-948) attached
1) ☐ hereto or 2) ☐ to Paper No./Mail Date	•	
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or in the	Office action of
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t		
6. DEPOSIT OF and/or INFORMATION about the depo- attached Examiner's comment regarding REQUIREMENT		
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5. ☐ Notice of Informal	Patent Application (PTO-152)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. 🛛 Interview Summa	ry (PTO-413),
3. M Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mail D 08), 7. ⊠ Examiner's Amen	
Paper No./Mail Date <u>7/14/04</u> 4. ☐ Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's Stater	nent of Reasons for Allowance
of Biological Material	9.	
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EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Stuart Auvinen on January 26, 2006.

The application has been amended as follows:

Claim 11, line 7, the word, "the" before "first resistance" has been changed to --a--.

Claim 13, lines 11-12, the phrase, "or a logical equivalent" has been deleted.

Claim 18, line 14, the word, --the-- has been added before "dynamic termination circuits".

Claim 19, line 8, the word, --the-- has been added before "segments".

Claim 21, line 1-2, the phrase, "further comprising: a" has been deleted and replaced by -- wherein the --. On line 2, the word, "that" between "substrate" and "drives" has been deleted.

Reasons for Allowance

Claims 1-25 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: the best prior art of record, Greeff et al., taken alone or in combination of other references, does not teach or fairly suggest a memory module comprising, among other things, a switch-signal generator, mounted on the substrate, receiving the chip-select signal and the clock signal, the switch-signal generator

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generating the switch signal in the active state synchronously to the clock signal when the chip-select signal is activated to select the plurality of memory chips for access by the main board, whereby the low-impedance path terminates the terminated signal line in response to the switch signal (claim 1); switch timing logic, receiving the chip-select signal and the clock signal, for pulsing activate the switch signal when the chip-select signal selects the plurality of synchronous memory chips for access during a portion of a period of the clock signal (claim 23); and switch-signal generator means, mounted on the substrate means, receiving the chip-select signal and the clock signal, the switch-signaling the switch generator means generating the switch signal in the active state synchronously to the clock signal when the chip-select signal is activated to select the plurality of memory chips for access by the main board, whereby the high-current resistor means terminates the terminated signal line means in response to the switch signal (claim 25), as set forth in the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel D. Chang whose telephone number is (571) 272-1801. The examiner can normally be reached on Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rexford Barnie can be reached on (571) 272-7492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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